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CONFIRMATION NO. 3926

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10/684,438	10/15/2003 RULE	327	2816	243570US-2 DIV

APPLICANTS

Tomoaki Yabe, Kawasaki-shi, JAPAN;

** CONTINUING DATA **********

This application is a DIV of 10/101,729 03/21/2002 PAT 6,661,279 TDC

** FOREIGN APPLICATIONS **********

JAPAN 2001-112463 04/11/2001 TDC

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
** 01/16/2004

Foreign Priority claimed ☒ yes ☐ no 35 USC 119 (a-d) conditions met ☒ yes ☐ no ☐ Met after Allowance Verified and Acknowledged Examiner's Signature Initials TDC	STATE OR COUNTRY JAPAN	SHEETS DRAWING 7	TOTAL CLAIMS 10	INDEPENDENT CLAIMS 2

ADDRESS
22850
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.
1940 DUKE STREET
ALEXANDRIA , VA
22314

TITLE

~~Semiconductor integrated circuit~~ Voltage Step Down Circuit with Reduced Leakage Current

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	☐ All Fees ☐ 1.16 Fees (Filing) ☐ 1.17 Fees (Processing Ext. of time) ☐ 1.18 Fees (Issue)